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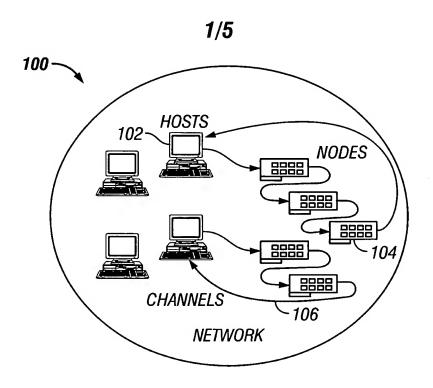


FIG. 1

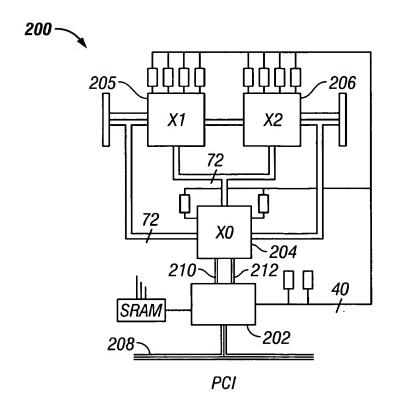


FIG. 2

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300 -QQQQ 40 -206 205° X1 *X2 X2* X1 72 ₂₀₆ 205 *X0 X0* -204 -204 ДD SRAM SRAM 202 202 POWERPC BUS A POWERPC BUS B

FIG. 3

400 -

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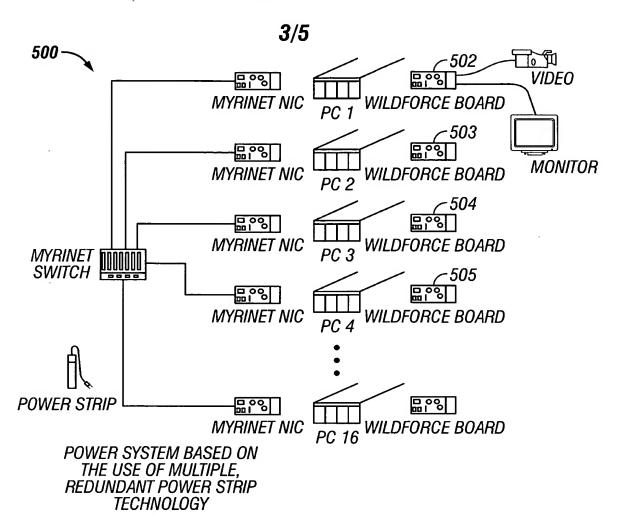


FIG. 5

600

```
for (int i=0;i<4;i++) {
    /* send bitstream for each ACS board */
    ACS_Configure(config[i],i,ring,&status);
    ACS_Clock_Set(clock,i,ring,&status);/* set clock speed */
    ACS_Run(i,ring,&status); /* start clock */
    ACS_Reset(i,ring,&status); /* send reset signal */
}
for (int i=0;i<4;i++) {
    /* write initial data to each board's memory */
    ACS_Write(databuf[i],datalen[i],i,brd_addr[i],ring,&status);
    /* then send an interrupt (or inta) signal #1 to the baord */
    ACS_Interrupt(i,1,ring,&status);
}
```

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700

```
/* use the ring to process the required number of images */
for (int i=0;i<NUM_IMAGES;i++) {
    /* send image onto channel associated with port 0*/
    ACS_Enqueue(image[i],IMAGESIZE,0,ring,&status);
    /* get resulting image from channel associated with port 1 */
    ACS_Dequeue(result_image[i],RESULT_SIZE,1,ring,&status);
}
```

FIG. 7

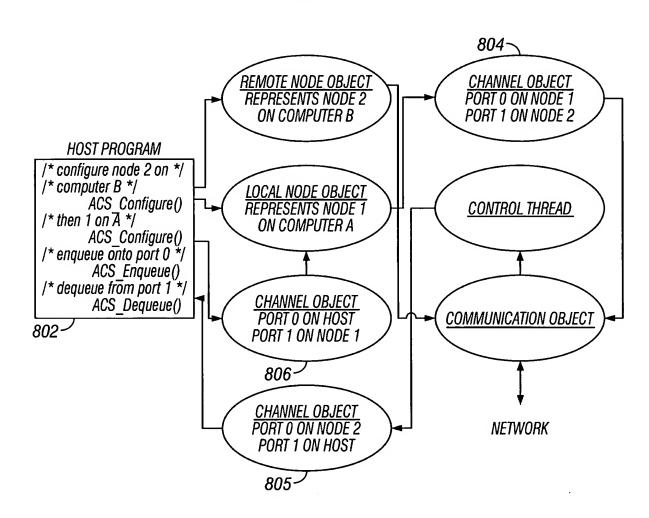


FIG. 8A

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